

Apple No. 10/796,597
Amdt date July 16, 2007
Reply to Office action of April 16, 2007

Amendments to the Specification:

Please amend the paragraph starting on line 7 of page 4 of the specification and corresponding to paragraph [0014] of the printed publication as follows:

When the reset period operates normally, the wall charges of the scan electrode Y and the sustain electrode X are erased, but unstable discharging may occur because of unstable resetting. The unstable discharging includes a first case in which discharging caused by self-erasing occurs at the time when voltage of the scan electrode Y falls to $V_{set} - V_s$ after strong discharging during a ramp rising period, a second case in which strong discharging occurs in a ramp rising period and a ramp falling period, and a third case in which strong discharging occurs during a ramp falling period.

Please amend the paragraph starting on line 21 of page 18 of the specification and corresponding to paragraph [0076] of the printed publication as follows:

When strong discharging has occurred in the ramp falling period 130, discharging occurs between the scan electrode Y and the sustain electrode X in the former part of the misfiring erase period 200, and the state of the wall charges becomes as shown in FIG. 6B. In this instance, when the reference voltage is applied to the scan electrode Y, and V_e volts to the sustain electrode X, discharging occurs between the scan electrode Y and the sustain electrode X because of a wall voltage V_{wxy4} formed by the distribution of the wall charges of FIG. 6B and the voltage difference between the scan electrode Y and the sustain electrode X. However, because of the narrow width of the V_e voltage pulse applied to the sustain electrode X, the charges formed by discharging are not accumulated to the scan electrode Y and the sustain electrode X, but are erased. Therefore, the state of the wall charged charges becomes as shown in FIG. 6C.